# Elliptic Curve Cryptography on Embedded Devices 

# Scalar Multiplication and Side-Channel Attacks 

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Séminaire Arithmétique et Théorie de l'Information Institut de Mathématiques de Luminy 01 / 2011

$$
\begin{aligned}
& \text { Institut de } \\
& \text { Mathématiques de } \\
& \text { Borde a ux }
\end{aligned}
$$

## Outline

(1) Elliptic Curve Cryptography

Generalities
Protocols
Points Representation and Formulas
Scalar Multiplication Algorithms
(2) Side-Channel Analysis

Introduction
Simple Side-Channel Analysis
Differential Side-Channel Analysis
Fault Analysis
(3) Countermeasures

SSCA Countermeasures
DSCA Countermeasures
FA Countermeasures
(4) Conclusion

## Inside Secure in (very) short



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## Elliptic Curve Equation

Considering a field $\mathbb{F}_{p}, p>3$, the points $(x, y)$ of $\mathcal{E} / \mathbb{F}_{p}: y^{2}=x^{3}+a x+b$ and the "point at infinity" $O$ form a group.


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## Elliptic Curve Discrete Logarithm Problem (ECDLP)

Given $P$ in $\mathcal{E}\left(\mathbb{F}_{p}\right)$ and $\alpha \cdot P, 1 \leq \alpha \leq \# \mathcal{E}\left(\mathbb{F}_{p}\right)$, find $\alpha$ ?
Much harder than DLP on finite fields, or factoring.

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| Security | $2^{80}$ | $2^{112}$ | $2^{128}$ | $2^{192}$ |
| :--- | :---: | :---: | :---: | :---: |
| EIGamal p/q | $160 / 1024$ | $224 / 2048$ | $256 / 3072$ | $384 / 8192$ |
| RSA | 1024 | 2048 | 3072 | 8192 |
| ECC | 160 | 224 | 256 | 384 |

Keylengths for roughly equivalent security

## Two Levels Arithmetic

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Points group of the elliptic curve

- $\mathcal{E}\left(\mathbb{F}_{p}\right)$ : point set
- additive law
- point additions and doublings


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## Base field

- $\mathbb{F}_{p}$ : equivalence classes of integers modulo $p$
- additive and multiplicative laws
- modular additions and multiplications


## Embedded Devices Constraints

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- At the base field level (addition formulas, points representation)


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## Arithmetic optimizations

- At the base field level (addition formulas, points representation)
- At the points group level (scalar multiplication algorithm)


## $\mathbb{F}_{p}$ Operations Theoretical Cost

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## Expensive operations

- Inversion (I)


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Significant operations

- Multiplication (M)
- Squaring (S, $\mathrm{S} / \mathrm{M} \approx 0.8$ )


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Negligible operations

- Addition (A)
- Subtraction (S)
- Negation (N)


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Negligible operations

- Addition $(A) \quad A / M \approx 0.2$ on most smart cards
- Subtraction (S)
- Negation (N)


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## Protocols

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## Elliptic Curve Digital Signature Algorithm (ECDSA)

Public : $\mathcal{E}(a, b, p, n=\# \mathcal{E}), P \in \mathcal{E}\left(\mathbb{F}_{p}\right), H$
InPut : $d$ and $m$
OUtPut: $(r, s)$
Choose at random $k$ in [1, $n-1$ ]
$P_{1} \leftarrow k \cdot P$
$r \leftarrow x_{P_{1}} \bmod n$
If $r \equiv 0 \bmod n$ restart from the beginning
$s \leftarrow k^{-1}(H(m)+d r) \bmod n$
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Return ( $r, s$ )

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$d=\frac{s \cdot k-H(m)}{r} \bmod n$

## Elliptic Curve Diffie-Hellman (ECDH) Key Exchange

$$
\mathcal{E}(a, b, p, n), P \in \mathcal{E}\left(\mathbb{F}_{p}\right)
$$

Alice
Choose at random $a \in[1, n-1]$

Bob
Choose at random $b \in[1, n-1]$

$$
\begin{gathered}
P_{a}=a \cdot P \\
P_{b} \\
P_{a b}=a \cdot P_{b}
\end{gathered}
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Card
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Terminal
Choose at random $b \in[1, n-1]$

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Keylengths : 192, 224, 256, 384, and 521 bits.

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Brainpool (BSI, Germany)
Keylengths : 160, 192, 224, 256, 320, 384, and 512 bits.

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Other standards (ANSI, ISO, IEEE, SECG) $\rightarrow$ NIST curves

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## Affine Representation

A point of the curve $\mathcal{E}: y^{2}=x^{3}+a x+b$ is represented as $(x, y)$. No representation for $O$

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\text { Add. : } 11+2 M+1 S \text {, Doubl. : } 1 I+2 M+2 S
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## Homogeneous Projective Representation

A point is represented by an equivalence class ( $X: Y: Z$ ).
$(X: Y: Z)$ and $(\lambda X: \lambda Y: \lambda Z), \lambda \neq 0$ represent the same point

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O=(0: 1: 0)
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Aff. $\rightarrow$ Hom. conversion :

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(x, y) \rightarrow(x: y: 1)
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Hom. $\rightarrow$ Aff. conversion :

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(X: Y: Z \neq 0) \rightarrow(X / Z, Y / Z)
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$(X: Y: Z \neq 0) \rightarrow(X / Z, Y / Z)$
Add. : $12 \mathrm{M}+2 \mathrm{~S}$, Doubl. : $6 \mathrm{M}+6 \mathrm{~S}$

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Jac. $\rightarrow$ Aff. conversion :
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Add. : $11 \mathrm{M}+5 \mathrm{~S}$, Doubl. : $2 \mathrm{M}+8 \mathrm{~S}$

## Modified Jacobian Projective Representation

Introduced in [Cohen, Miyaji \& Ono, Efficient elliptic curve exponentiation using mixed coordinates, Asiacrypt 1998].

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Plus an extra coordinate $\left(X: Y: Z: a Z^{4}\right)$.

## Modified Jacobian Projective Representation

Introduced in [Cohen, Miyaji \& Ono, Efficient elliptic curve exponentiation using mixed coordinates, Asiacrypt 1998].

Based on the Jacobian projective representation.
Plus an extra coordinate $\left(X: Y: Z: a Z^{4}\right)$.
Faster doubling than Jacobian projective : $3 \mathrm{M}+5 \mathrm{~S}$
But slower addition : $13 \mathrm{M}+7 \mathrm{~S}$

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## Double \& Add Algorithm

Left-to-Right

INPUT: $\quad P \in \mathcal{E}\left(\mathbb{F}_{p}\right)$,

$$
k=\left(k_{\ell-1} \ldots k_{1} k_{0}\right)_{2}
$$

OUTPUT: $k \cdot P$
$Q \leftarrow O$
For $i$ from $\ell-1$ to 0 do
$Q \leftarrow 2 Q$
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## On average :

$Q \leftarrow O$
For $i$ from $\ell-1$ to 0 do

$$
\ell \cdot \mathrm{dbl}+\frac{\ell}{2} \cdot \mathrm{add}
$$

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## NAF Multiplication

Signed binary representation.
Minimize the number of non-zero digits ( $1 / 3 \mathrm{vs} 1 / 2$ ).

## Example :

$$
187=10111011^{(2)}=10 \overline{1} 000 \overline{1} 0 \overline{1}^{(N A F)}
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Interest

- Minimize the number of additions
- $P \rightarrow-P$ is cheap : $(X: Y: Z) \rightarrow(X:-Y: Z)$


## NAF Multiplication

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$\begin{array}{ll}\text { INPUT : } & P \in \mathcal{E}\left(\mathbb{F}_{p}\right), \\ & k=\left(k_{\ell-1} \ldots k_{1} k_{0}\right)_{\text {NAF }}\end{array}$
OUTPUT: k.P
$Q \leftarrow O$
$R \leftarrow P$
For $i$ from 0 to $\ell-1$ do
If $k_{i}=1$ then
$Q \leftarrow Q+R$
If $k_{i}=-1$ then
$Q \leftarrow Q+(-R)$
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INPUT: $\quad P \in \mathcal{E}\left(\mathbb{F}_{p}\right)$,

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k=\left(k_{\ell-1} \ldots k_{1} k_{0}\right)_{\mathrm{NAF}} \quad \underline{\text { Cost }: ~}
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- $Q$ in Jacobian coordinates


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Precompute $3 P, 5 P, \ldots$ to process several scalar bits at a time. Can be combined with the NAF method.

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Heavy precomputations.
Too expensive for the ECDSA in the embedded context.

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Can be combined with the NAF method.

## DBNS, multibase NAF...

Heavy precomputations.
Too expensive for the ECDSA in the embedded context.

## Co-Z Addition

Euclidean Addition Chains [Meloni, WAIFI 2007] Co-Z binary ladder [Goundar, Joye \& Miyaji, CHES 2010]

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## A chip in details



## A chip in details



## Attack Bench

Non Invasive Attacks


## Simple Analyse Example

Leakage on Performed Operations


RSA exponentiation

## Simple Analyse Example

## Leakage on Manipulated Data



FIGURE 2. Number of Bit Transitions versus Power Consumption
These results show how the data effects the power levels. The nine overlayed waveforms correspond to the power traces of different data being accessed by an LDA instruction. These results were obtained by averaging the power signals across 500 samples in order to reduce the noise content. The difference in voltage between $i$ transitions and i+1 transitions is about 6.5 mV .

## Milestones

- Timing Attacks [Kocher, Timing Attacks on Implementations of Diffie-Hellman, RSA, DSS, and Other Systems, Crypto 1996]
- Fault Attacks [Boneh et al., On the Importance of Checking Cryptographic Protocols for Faults, Eurocrypt 1997]
- SPA and DPA [Kocher et al., Differential Power Analysis, Crypto 1999]


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- DFA on ECC [Biehl et al., Differential Fault Attacks on Elliptic Curve Cryptosystems, Crypto 2000]
- DPA on RSA [den Boer et al., A DPA Attack Against the Modular Reduction within a CRT Implementation of RSA, CHES 2002]


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- DPA on RSA [den Boer et al., A DPA Attack Against the Modular Reduction within a CRT Implementation of RSA, CHES 2002]
- CPA [Brier et al., Correlation Power Analysis with a Leakage Model, CHES 2004]
- CPA on PK [Amiel et al., Power Analysis for Secret Recovering and Reverse Engineering of Public Key Algorithms, SAC 2007]


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## Simple Analysis Principle

Measure one side-channel leakage s function of $t$ and consider the curve $s(t)$.

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## SPA/SEMA

- depicts the behavior of the chip depending on the performed operations / manipulated data


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## SPA/SEMA

- depicts the behavior of the chip depending on the performed operations / manipulated data
- each measure enables direct reading


## Example

Left-to-Right Double \& add Algorithm Analysis
$Q \leftarrow O$
For $i$ from $\ell-1$ to 0 do $Q \leftarrow 2 Q$
If $k_{i}=1$ then $Q \leftarrow Q+P$

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## Example

Left-to-Right Double \& add Algorithm Analysis
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## Differential Side-Channel Analysis

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## Differential Analysis Principle

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$s_{1}(t), s_{2}(t), \ldots, s_{n}(t)$.

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- targets a same operation on all curves but involving different data



## Differential Analysis Principle

Measure $n$ times a side-channel leakage $s$ function of $t$ and consider the curves
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- align vertically the curves on the targeted operation



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- targets a same operation on all curves but involving different data
- align vertically the curves on the targeted operation
- process the curves with statistical treatment



## Differential Analysis

Statistical Treatment

Depending on some known and variable input of the algorithm and of a few bits of the secret input.

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- Iterate until peaks are found


## Differential Analysis

## Statistical Treatment

## Example

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## Example



## Differential Analysis

Statistical Treatment

## Example

$$
\begin{array}{cc}
C_{1} & P_{1} \\
C_{2} & P_{2} \\
\vdots & \vdots \\
C_{N} & P_{N}
\end{array}
$$

## Differential Analysis

Statistical Treatment

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Guess : $k_{i}=0$


## Differential Analysis

Statistical Treatment

## Example

\[

\]

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\[

\]

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\[

\]



## Differential Analysis

Statistical Treatment

## Example

Guess : $k_{i}=0$

| $C_{1}$ | $P_{1}$ | $Q_{1}^{i}$ | $\rightarrow$ | $S_{0}$ |
| :---: | :---: | :---: | :---: | :---: |
| $C_{2}$ | $P_{2}$ | $Q_{2}^{j}$ | $\rightarrow$ | $S_{0}$ |
| $\vdots$ | $\vdots$ | $\vdots$ |  | $\vdots$ |
| $C_{N}$ | $P_{N}$ | $Q_{N}^{i}$ | $\rightarrow$ | $S_{1}$ |



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Guess : $k_{i}=1$


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\[

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Statistical Treatment

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$$
\begin{aligned}
& \text { Guess : } k_{i}=1 \\
& \begin{array}{ccclll}
\vdots & \vdots & \vdots & & \vdots \\
C_{N} & P_{N} & Q_{N}^{i} & \rightarrow & S_{0}
\end{array}
\end{aligned}
$$

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- For each possible value (guess) :
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- average the correlation curves and apply a threshold
- Iterate until the threshold is reached


## Outline



```
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Scalar Multiplication Algorithms
```

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DSCA Countermeasures
FA Countermeasures
4. Conclusion

## Fault Attacks on Scalar Multiplication

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- Inject a fault : $x_{P} \leftarrow x_{P \prime}$
- Since $b$ is not involved in the scalar multiplication, $P^{\prime} \in \mathcal{E}^{\prime}\left(\mathbb{F}_{p}\right)$, with $\mathcal{E}^{\prime}: y^{2}=x^{3}+a x+b^{\prime}$ and $b^{\prime}=y_{P}{ }^{2}-x_{P}^{\prime 3}-a x_{P}^{\prime}$


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- Iterate and apply the chinese reminder theorem to recover $k$.


## Outline

# Generalities <br> Protocols <br> Points Representation and Formulas <br> Scalar Multiplication Algorithms 

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- Improved ECC pattern [Giraud and Verneuil, 2010]


## Regular Algorithms

Double \& add always
$Q, T \leftarrow O$
For $i$ from $\ell-1$ to 0 do
$Q \leftarrow 2 Q$
If $k_{i}=1$ then
$Q \leftarrow Q+P$
Else

$$
T \leftarrow Q+P
$$

## Return $Q$

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On average :

$\ell \cdot \mathrm{dbl}+\ell \cdot$ add
Prone to safe errors.

## Regular Algorithms

Montgomery ladder
$Q_{1} \leftarrow P$
$Q_{2} \leftarrow 2 P$
For $i$ from $I-2$ to 0 do
$Q_{1-k_{i}} \leftarrow Q_{1}+Q_{2}$ $Q_{k_{i}} \leftarrow 2 Q_{i}$
Return $Q_{1}$

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Montgomery ladder
$Q_{1} \leftarrow P$
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For $i$ from $/-2$ to 0 do $Q_{1-k_{i}} \leftarrow Q_{1}+Q_{2}$ $Q_{k_{i}} \leftarrow 2 Q_{i}$
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$Q_{1} \leftarrow P$
$Q_{2} \leftarrow 2 P$
For $i$ from $/-2$ to 0 do

$$
\begin{aligned}
& Q_{1-k_{i}} \leftarrow Q_{1}+Q_{2} \\
& Q_{k_{i}} \leftarrow 2 Q_{i}
\end{aligned}
$$

Return $Q_{1}$
Trick :
$Y_{1}$ and $Y_{2}$ computation can be avoided.

- Brier \& Joye, PKC 2002
- Izu \& Takagi, PKC 2002
- Fischer et al., ePrint 2002



## Unified Formulas

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A single formula for addition and doubling

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- S, M, S, S, S, M, S, S, M, S, M, ...


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- M, M, M, M, M, M, M, M, M, M, M, ...
$\rightarrow$ Cost


## Atomicity for Elliptic Curves

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\author{

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No more squarings :(

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- Addition
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No more squarings :(
Many dummy additions/negations :(

## Longa Atomicity

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## Other patterns

In [Longa, Accelerating the Scalar Multiplication on Elliptic Curve Cryptosystems over Prime Fields, 2007] are proposed 2 new patterns :

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- Addition
- Multiplication
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- Addition
- Addition
- Squaring
- Negation
- Addition
- Multiplication
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- Addition


## Atomicity Improvement

Full paper : [Giraud \& Verneuil, Atomicity Improvement for Elliptic Curve Scalar Multiplication, CARDIS 2010]

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## Two steps

- First define the largest atomic pattern possible
- Then remove as many possible dummy operations


## Advantages

- Potentially applicable to every algorithm (no curve restriction)
- Prevents from the SPA at a lower cost than classical atomicity


## Atomic-Joye's Multiplication

## Best pattern

|  | Add. 1 | Add. 2 | Dbl. |
| :---: | :---: | :---: | :---: |
| Sq. | $R_{1} \leftarrow z_{2}{ }^{2}$ | $R_{1} \leftarrow R_{6}{ }^{2}$ | $R_{1} \leftarrow x_{1}{ }^{2}$ |
| Add. |  |  | $R_{2} \leftarrow Y_{1}+Y_{1}$ |
| Mult. | $R_{2} \leftarrow Y_{1} \cdot Z_{2}$ | $R_{4} \leftarrow R_{5} \cdot R_{1}$ | $Z_{2} \leftarrow R_{2} \cdot Z_{1}$ |
| Add. |  |  | $R_{4} \leftarrow R_{1}+R_{1}$ |
| Mult. | $R_{5} \leftarrow Y_{2} \cdot Z_{1}$ | $R_{5} \leftarrow R_{1} \cdot R_{6}$ | $R_{3} \leftarrow R_{2} \cdot Y_{1}$ |
| Add. |  |  | $R_{6} \leftarrow R_{3}+R_{3}$ |
| Mult. | $R_{3} \leftarrow R_{1} \cdot R_{2}$ | $R_{1} \leftarrow Z_{1} \cdot R_{6}$ | $R_{2} \leftarrow R_{6} \cdot R_{3}$ |
| Add. |  |  | $R_{1} \leftarrow R_{4}+R_{1}$ |
| Add. | * |  | $R_{1} \leftarrow R_{1}+W_{1}$ |
| Sq. | $R_{4} \leftarrow z_{1}{ }^{2}$ | $R_{6} \leftarrow R_{2}{ }^{2}$ | $R_{3} \leftarrow R_{1}{ }^{2}$ |
| Mult. | $R_{2} \leftarrow R_{5} \cdot R_{4}$ | $Z_{3} \leftarrow R_{1} \cdot Z_{2}$ | $R_{4} \leftarrow R_{6} \cdot X_{1}$ |
| Add. |  | $R_{1} \leftarrow R_{4}+R_{4}$ | $R_{5} \leftarrow W_{1}+W_{1}$ |
| Sub. | $R_{2} \leftarrow R_{2}-R_{3}$ | $R_{6} \leftarrow R_{6}-R_{1}$ | $R_{3} \leftarrow R_{3}-R_{4}$ |
| Mult. | $R_{5} \leftarrow R_{1} \cdot x_{1}$ | $R_{1} \leftarrow R_{5} \cdot R_{3}$ | $W_{2} \leftarrow R_{2} \cdot R_{5}$ |
| Sub. |  | $X_{3} \leftarrow R_{6}-R_{5}$ | $\chi_{2} \leftarrow R_{3}-R_{4}$ |
| Sub. | * | $R_{4} \leftarrow R_{4}-X_{3}$ | $R_{6} \leftarrow R_{4}-X_{2}$ |
| Mult. | $R_{6} \leftarrow X_{2} \cdot R_{4}$ | $R_{3} \leftarrow R_{4} \cdot R_{2}$ | $R_{4} \leftarrow R_{6} \cdot R_{1}$ |
| Sub. | $R_{6} \leftarrow R_{6}-R_{5}$ | $Y_{3} \leftarrow R_{3}-R_{1}$ | $Y_{2} \leftarrow R_{4}-R_{2}$ |

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- Scalar blinding : $k^{\prime}=k+r \# \mathcal{E}\left(\mathbb{F}_{p}\right)$
- Point coordinates blinding : $(X: Y: Z)=\left(r^{2} X: r^{3} Y: r Z\right), r \neq 0$


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Classical countermeasures :

- Scalar blinding : $k^{\prime}=k+r \# \mathcal{E}\left(\mathbb{F}_{p}\right)$
- Point coordinates blinding : $(X: Y: Z)=\left(r^{2} X: r^{3} Y: r Z\right), r \neq 0$
- Random curve isomorphism :
$a^{\prime} \leftarrow r^{4} a$
$b^{\prime} \leftarrow r^{6} b$
$P^{\prime} \leftarrow\left(r^{2} X_{P}, r^{3} Y_{P}, r Z_{P}\right)$
$Q \leftarrow\left(x_{Q^{\prime}} / r^{2}, y_{Q^{\prime}} / r^{3}\right)$


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## Fault Protection

## Classical countermeasures:

- Redundancy, verification...
- Verify that $P, Q \in \mathcal{E}\left(\mathbb{F}_{p}\right)$.


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# SSCA Countermeasures <br> DSCA Countermeasures <br> FA Countermeasures 

(4) Conclusion

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- Scalar multuplication efficiency has been extensively studied.


## Conelusion

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- Edwards curve standardization?
- Research on side-channel attacks keeps progressing.
- Using security models for proving the resistance against attacks?


# Thank you for your attention! 



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## Additions Cost on a Chip



192-bit integers
$A / M \approx 0.2, S=A$, and $N / M \approx 0.1$

